

ABSTRACT

An amplifier circuit (100) includes a driver stage (120) with at least an active
5 device (140) for pre-amplification and output of a pre-amplified signal; and an output stage
(160) with at least an active device (180) for further amplification of the pre-amplified
signal and output of an amplified signal. A detector (190) measures levels of forward and
reflected parts of the amplified signal, and a control circuit (145) modifies DC levels or
offsets of the pre-amplified and/or amplified signals to substantially maintain linearity of
10 the amplifier circuit (100) with load variations. The control circuit (145) further
independently and selectively controls and adjusts the DC bias at the input of the active
devices (140, 180) of the driver and output stages (120, 160) as a function of the levels of
the forward and reflected signals to substantially maintain linearity of the amplifier circuit
(100) with load variations.

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